SHEET F

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A
  0:  <>  | au --> db --> aob,au,pc | trix | (dbin) --> ab* --> alu,alub | dbi | edb --> dbin,irc | 2n | dxdy | c5 | bcsm1 | bcsm1
  1:  <   | au --> aob,pc | irix | (rx) --> ab --> at,dcr | dbi | (ry) --> db --> au | x | ukry | 12f | bcsr1 | exge1
  2:  <*  | au --> aob | irix | (dbin) --> ab* --> alu,alub | dbi | (dcr) --> dbd --> alu | 2n | dxdy | 320 | bclm1 | bclm1
  3:  <   | au --> aob,pc | irix | (rx) --> ab --> at,dcr | dbi | (ry) --> db --> au | x | ukry | 12b | bclr1 | exge1
  4:  <   | au --> db --> aob,au,pc | irix | (dbin) --> ab* --> alu | dbi | (dcr) --> dbd --> alu | 1i | dxuk | 32f | btsm1 | btsm1
      mmrw3
  5:  <*  | au --> aob,pc | irix | (rydl) --> dbd --> alu,alu | dbi | 2n | dxry | 153 | tasr1 | tasr1
  6:  <   | au --> aob,pc | irix | (rx) --> ab --> at,dcr | dbi | (ry) --> db --> au | x | ukry | 127 | btsr1 | exge1
  7:  | (at) --> db --> au | np | (dbin) --> dbd --> alu | db | ftu --> ab --> alu | 2n | dxdy | 0 --> au | 4e | tasm1 | tasm1
  8:  <   | au --> aob,pc | irix | (rxdl) --> dbe --> au | dbi | (ryh) --> ab --> alu | 1n | rxry | -1 --> alu | 0 --> au | 302 | srrw1 | srrw1
  9:  <   | au --> aob,pc | irix | ftu --> dbe --> au | dbi | (ryh) --> ab --> alu | 1n | dxry | -1 --> alu | 0 --> au | 305 | srll1 | sriw1
      srrl2

B
  0:  <>  | alu --> dob | twop | (alub) --> alu | a1 | (at) --> * --> aob | 1i | (dcr) --> db* --> alu | (ir) --> ird | c6 | bcsm2 | bcsm2
  1:  >   | (dcr) --> dbd --> alu | frix | edb --> dbin,irc | bc | (pc) --> db --> au | 2n | (ryl) --> ab* --> alu,alub | +2 --> au | 382 | bcsr2 | bcsr2
          |---> bcsr4 if dcr 4 =0
          |---> bcsr3 if dcr 4 =1
  2:  >   | alu --> ab* --> alu | frix | au --> db --> au,pc | db | (dcr) --> dbd --> alu | 4n | edb --> dbin,irc | +2 --> au | 368 | bclm2 | bclm2
      bcsm2
  3:  >   | (dcr) --> dbd --> alu | frix | edb --> dbin,irc | bc | (pc) --> db --> au | 2n | (ryl) --> ab* --> alu,alub | +2 --> au | 386 | bclr2 | bcsr2
          |---> bclr3 if dcr 4 =1
          |---> bclr4 if dcr 4 =0
  5:  >*  | alu --> * --> ryl | frix | (alub) --> alu | a1 | au --> db --> au | 1if | edb --> dbin,irc | bc | -1 --> alu | 1n | +2 --> au | 88 | tasr2 | tasr2
  6:  >   | edb --> dbin,irc | frix | (pc) --> db --> au | bc | (ryl) --> ab* --> alu,alub | 1n | -1 --> alu | +2 --> au | 174 | btsr2 | btsr2
          |---> btsr3 if dcr[4]=1
          |---> bcsr4 if dcr[4]=0
  7:  <> m | alu --> dob | twop | au --> aob | db | (dbin) --> ab* --> alu | 1i | (pc) --> db --> au | -1 --> alu | +2 --> au | 4f | tasm2 | tasm2
      b
  8:  >   | alu --> dbd --> alue | frix | au --> db --> au | bc | edb --> dbin,irc | 1i | (rydl) --> ab* --> alu | -1 --> alu | -1 --> au | 303 | srrw2 | srrw2
          |---> srrw3 if au/=0
          |---> nbcr3 if au=0
  9:  <   | au --> aob,pc | irix | (rxdl) --> dbe --> au | dbi | (ryh) --> ab --> alu | 1n | rxry | -1 --> alu | 0 --> au | 306 | srrl1 | srrw1

C
  0:  <>  | alu --> ab --> ryl | np | (alub) --> alu | a1 | (dcr) --> db* --> alu | 1i | (ir) --> ird | ea | bcsm4 | bcsm4
  1:  | (dcr) --> db* --> alu | np | (ryh) --> ab --> alu,alub | db | 2n | aa | bcsr3 | bcsr3
      | bcsr5
  2:  | alu --> ab* --> alu | np | (dcr) --> db* --> alu | db | 4n | 2f1 | bclr4 | bclr4
      | bcsr4
  3:  | (dcr) --> db* --> alu | np | (ryh) --> ab --> alu,alub | db | 2n | 2b1 | bclr3 | bcsr3
      | bclr5
  4:  | (dbin) --> ab* --> alu | np | 0 --> alu | db | 5n | 6 | ldmx1 | aixw0
      | ldmx2
  5:
  6:  | (dcr) --> db* --> alu | np | (ir) --> ird | a1 | (ryh) --> ab --> alu | 1i | 6a | btsr3 | btsr3
  7:
  8:  | alu --> ab* --> alu | np | au --> db --> au | bc | -1 --> alu | 4i | -1 --> au | 2fc | srrw3 | srrw3
          |---> srrw3 if au/=0
          |---> nbcr3 if au=0
  9:  >   | frix | alu --> dbd --> alue | bc | au --> db --> au | 1i | edb --> dbin,irc | -1 --> alu | (rydl) --> ab* --> alu | -1 --> alu | -1 --> au | 307 | srrl2 | srrw2
          |---> srrl3 if au/=0
          |---> srrl4 if au=0

E
  0:  | alu --> ab --> ryh | np | (alub) --> alu | a1 | (dcr) --> db* --> alu | 1i | (ir) --> ird | 329 | bcsr5 | bcsr5
      bcsr5
  1:  | alu --> ab* --> alu | np | (dcr) --> db* --> alu | db | 4n | 2f5 | bclr5 | bclr4
  2:  <   | irin | alu --> *e --> au | bc | au --> aob | x | (rya) --> * --> au | 15a | ldmx2 | aixw1
          |---> ldmx3 if irc[11]=1
          |---> ldmx4 if irc[11]=0
  3:  <>  | trim | au --> db --> aob,au,pc | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | rpdt | +2 --> au | 1f9 | lmaw1 | malw3
  4:
  5:  <>  | trin | au --> db --> aob,au | db | (dbin) --> abe --> ry | x | edb --> irc | rpdy | +2 --> au | 36a | lmaw2 | lmaw2
      ldmr2
  6:  <>  | trim | au --> db --> aob,au,pc | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | rppy | +2 --> au | 1fd | ldmd1 | malw3
  7:  <   | irin | au --> aob,pc | 1i | ftu --> ab --> alu | 2n | dxdy | (ryh) --> ab --> alu | 0 --> au | 301 | sriw1 | sriw1
      srrw2
  8:  | alu --> ab* --> ryd1 | np | (alue) --> db --> alub,ryh | db | x | 278 | srrl4 | srrl4
  9:  | np | nbcr3 au=0 | alu --> ab* --> alu | bc | au --> db --> au | 4i | -1 --> alu | -1 --> au | 2f8 | srrl3 | srrw3
          |---> srrl3 if au/=0
          |---> srrl4 if au=0

F
  0:  | 1e5 | smal1 | malw3
  1:  <>  | trim | au --> db --> aob,au | db | (dbin) --> ab --> ath | x | edb --> dbin | rpdy | +2 --> au | 230 | smal2 | ablw1
  2:  <>  | trim | au --> db --> aob,au | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | rpdy | +4 --> au | 4a | stmx1 | stmx1
  3:  >   | frin | au --> * --> au | db | edb --> irc | x | (rxl) --> *e --> au | 27 | ldmx4 | ldmx4
  4:  >   | frin | au --> * --> au | db | edb --> irc | x | (rx) --> * --> au | e7 | ldmx3 | ldmx3
  5:  <>  | trin | au --> db --> aob,au,pc | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | rppy | +2 --> au | 1ed | smaw1 | malw3
  6:  <   | irin | au --> aob | db | (dbin) --> *e --> au | x | (rya) --> * --> au | 384 | ldmd2 | lead1
  7:  <>  | trim | au --> db --> aob,au,pc | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | rppy | +2 --> au | 1fd | ldmd1 | malw3
  8:  <   | np | (alub) --> alu | bc | (ir) --> ird | a1 | (pc) --> db --> au | 1f | -1 --> alu | +2 --> au | 43 | srrl5 | srrl5
  9:  <*  | irix | au --> db --> aob,au,pc | bc | (dbin) --> ab* --> alu | 4i | -1 --> alu | +2 --> au | 3ea | sftm1 | tsmw1
      sftm2

G
  0:  <   | irin | au --> db --> aob,au | db | (dbin) --> abd --> atl | x | +2 --> au | 67 | smal3 | smal3
      smaw3
  1:  | np | au --> db --> au,pc | db | (dbin) --> ab* --> alu,atl | 5n | 0 --> alu | -2 --> au | 156 | stmx2 | stmx2
      stmx3
  2:  <   | irin | au --> db --> aob,au | db | (dbin) --> abe --> at | x | +2 --> au | 3e1 | smaw2 | smaw2
      smaw3
  3:  <>  | trim | au --> db --> aob,au,pc | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | rppy | +2 --> au | 1f1 | stmd1 | malw3
  4:  <m  | iruk | au --> ab --> aob,at | db | (dbin) --> abd --> alu | x | (pc) --> db --> au | -1 --> alu | +4 --> au | 277 | ldmx5 | adsw2
      ldmx6
  5:  <>  | trin | au --> db --> aob,au,pc | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | rpry | +2 --> au | 135 | ldmr1 | malw3
      ldmr2
  6:  >   | frin | au --> ab --> at | db | edb --> irc | x | (pc) --> db --> au | +4 --> au | 157 | ldmd3 | ldmd3
      ldmd4
  7:  <>  | trim | au --> db --> aob,au,pc | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | +2 --> au | 1e9 | lmal1 | malw3
      lmal2
  8:
  9:  <*  | irix | au --> db --> aob,au,pc | bci | (dbin) --> ab* --> alu | 1i | -1 --> alu | +2 --> au | 3a5 | push1 | stmr1
      sftm2

H
  0:  <   | irin | alu --> *e --> au | bc | au --> aob | x | (rya) --> * --> au | 15e | stmx3 | aixw1
          |---> stmx4 if irc[11]=0
          |---> stmx5 if irc[11]=1
  1:  >   | frin | (at) --> db --> au | bc | edb --> dbin,irc | x | reset pren | 257 | smaw3 | smaw3
          |---> mmrw2 if end
          |---> stmr4 if 1*/end
                stmr5 if w*/end
  2:  <>  | trim | au --> db --> aob,au,pc | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | rpry | +2 --> au | 1f1 | stmd1 | malw3
  3:  >   | fruk | au --> pc | bc | edb --> dbin | x | +2 --> au | reset pren | 293 | ldmx6 | ldmx6
          |---> ldmr3 if l*/end
          |---> mmrw2 if end
          |---> ldmr4 if w*/end
  4:
  5:  <>  | trop | au --> pc | bc | edb --> dbin | x | (ry) --> db --> aob,at,au | +2 --> au | reset pren | 103 | ldmr2 | ldmr2
          |---> ldmr4 if w*/end
          |---> ldmr3 if l*/end
          |---> mmrw2 if end
  6:  <>  | truk | (at) --> db --> aob,au | bc | au --> pc | x | edb --> dbin | +2 --> au | reset pren | 2fa | ldmd4 | ldmd4
          |---> mmrw2 if end
          |---> ldmr3 if l/end
          |---> ldmr4 if w*/end
  7:  <>  | twop | au --> db --> aob,au,pc | db | (dbin) --> ab --> ryh | x | edb --> dbin | +2 --> au | 29d | lmal2 | e#l1
  8:  <>  | trim | au --> db --> aob,au,pc | db | (dbin) --> ab --> ftu | x | edb --> dbin,irc | rpry | +2 --> au | 131 | popm1 | malw3
  9:  >*  | frix | alu --> ab* --> alu | bc | au --> db --> au | 4i | -1 --> alu | edb --> dbin,irc | 0 --> au | 41 | sftm2 | sftm2
      morw2

I
  0:  <   | irin | au --> db --> aob,au | db | (dbin) --> ab --> ftu | x | +2 --> au | reset pren | 3a1 | stmr1 | stmr1
  1:  >   | frin | au --> pc | db | edb --> irc | x | (rya) --> db --> au | 0 --> au | reset pren | 48 | stmr2 | stmr2
          |---> stmr4 if 1*/end
          |---> mmrw2 if end
          |---> stmr5 if w*/end
  2:  >   | frin | au --> * --> au | db | edb --> dbin,irc | x | reset pren | 13c | stmx4 | aixw2
          |---> stmr5 if w*/end
          |---> mmrw2 if end
          |---> stmr4 if l*/end
          |---> stmr5 if w*/end
  3:  >   | frin | au --> * --> au | db | edb --> dbin,irc | x | reset pren | 1fc | stmx5 | aixw4
          |---> mmrw2 if end
          |---> stmr4 if l*/end
          |---> stmr5 if w*/end
  4:  >   | frin | au --> pc | db | (dbin) --> *e --> au | x | edb --> irc | (rya) --> * --> au | 47 | stmd3 | stmd3
          |---> mmrw2 if end
          |---> stmr4 if l*/end
          |---> stmr5 if w*/end
  5:  >   | truk | au --> db --> aob,at,au | db | (dbin) --> ab --> rxh | x | edb --> dbin | +2 --> au | 3e8 | ldmr3 | o#l1
  6:  <>  | truk | au --> db --> aob,at,au | db | (dbin) --> abe --> rx | x | edb --> dbin | +2 --> au | 3a8 | ldmr4 | ldmr4
          |---> ldmr3 if l*/end
          |---> ldmr4 if w*/end
  7:  <>  | trop | au --> db --> aob,au,pc | db | (dbin) --> ab --> rxh | x | edb --> dbin | +2 --> au | reset pren | 3ec | popm3 | o#l1
          |---> popm3 if l*/end
          |---> popm6 if end
          |---> popm4 if w/end
  8:  <>  | trop | au --> db --> aob,at,au | db | (dbin) --> abe --> rx | x | edb --> dbin | +2 --> au | reset pren | 3ac | popm4 | ldmr4
          |---> popm3 if l/end
          |---> popm6 if end
          |---> popm4 if w/end
  9:  <   | irin | au --> db --> aob,au | db | (dbin) --> ab --> ftu | x | +2 --> au | reset pren | 32c | popm6 | popm6
      popm5
  9:  >   | frin | au --> pc | db | edb --> irc | x | (rya) --> db --> at,au | -2 --> au | reset pren | 25f | push2 | push2
          |---> push3 if end
          |---> push4 if l*/end
          |---> push5 if w*/end

J
  0:  <   | irin | au --> db --> aob,au | db | (dbin) --> ab --> ftu | x | +2 --> au | 3a1 | stmr1 | stmr1
  1:  >   | frin | au --> pc | db | edb --> irc | x | (rya) --> db --> au | 0 --> au | 48 | stmr2 | stmr2
          |---> stmr4 if 1*/end
          |---> mmrw2 if end
          |---> stmr5 if w*/end
  2:  >   | frin | au --> * --> au | db | edb --> dbin,irc | x | reset pren | 13c | stmx4 | aixw2
          |---> stmr5 if w*/end
          |---> mmrw2 if end
          |---> stmr4 if l*/end
          |---> stmr5 if w*/end
  3:  >   | frin | au --> * --> au | db | edb --> dbin,irc | x | reset pren | 1fc | stmx5 | aixw4
          |---> mmrw2 if end
          |---> stmr4 if l*/end
          |---> stmr5 if w*/end
  4:  >   | frin | au --> pc | db | (dbin) --> *e --> au | x | edb --> irc | (rya) --> * --> au | 47 | stmd3 | stmd3
          |---> mmrw2 if end
          |---> stmr4 if l*/end
          |---> stmr5 if w*/end
  5:  >   | frin | edb --> dbin | db | (pcl) --> ab --> alu | bc | (sp) --> db --> au | -1 --> alu | -2 --> au | +2 --> psws | 3e8 | ldmr3 | o#l1
  6:  >   | frin | alu --> ab* --> alu | db | edb --> dbin,irc | x | 0 -- au | reset pren | 3a8 | ldmr4 | ldmr4
          |---> mmrw2 if end
          |---> ldmr3 if l*/end
          |---> ldmr4 if w*/end
  7:  <>  | trop | au --> db --> aob,at,au | db | (dbin) --> abe --> rx | x | edb --> dbin | +2 --> au | reset pren | 3ac | popm4 | ldmr4
          |---> popm3 if l*/end
          |---> popm6 if end
          |---> popm4 i f w*/end
  8:  <   | irix | (at) --> ab --> ry | db | (pc) --> db --> aob,au | x | +2 --> au | 32c | popm6 | popm6
      mmrw3
  9:  >   | frin | au --> pc | db | edb --> irc | x | (rya) --> db --> at,au | -2 --> au | reset pren | 25f | push2 | push2
          |---> push3 if end
          |---> push4 if l/end
          |---> push5 if w*/end

K
  0:  <>  | twop | au --> db --> aob,au | db | (rxh) --> ab --> dob | x | +2 --> au | e1 | stmr4 | stmr4
  1:  <>  | twop | au --> db --> aob,au | bc | (rxl) --> ab --> dob | x | +2 --> au | a1 | stmr5 | stmr6
          |---> stmr5 if w/end
          |---> mmrw2 if end
          |---> stmr4 if l/end
  2:  <*  | irix | au --> db --> aob,pc | bci | (dbin) --> ab* --> dcr | 1n | 0 --> alu | dxry | 17f | sccr1 | sccb1
          |---> sccr2 if cc=true
          |---> roaw2 if cc=false
  3:  <   | irix | au --> aob [alub | bci | (dcr) --> dbd --> alu | 2n | (psw) --> ftu | spdy | 324 | trpv1 | bclm1
          |---> trpv2 if v=0
          |---> trpv3 if v=1
  4:  <*  | irix | au --> db --> aob,pc | bci | (dbin) --> ab* --> dcr | 1n | 0 --> alu | dxdy | 17b | sccb1 | sccb1
          |---> sccb2 if cc=true
          |---> sccb3 if cc=false
  5:  <>  | trop | au --> db --> aob,at,au | bc | (dbin) --> ab --> rx1 | x | edb --> dbin | +2 --> au | reset pren | 256 | popm5 | ldmr5
          |---> popm3 if l/end
          |---> popm6 if end
          |---> popm4 if w/end
  6:
  7:  <>  | twop | au --> db --> aob,at,au | db | (rxl) --> ab --> dob | x | -2 --> au | eb | push4 | push4
      push6
  8:  <>  | twop | au --> db --> aob,at,au | bc | (rxl) --> ab --> dob | x | -2 --> au | reset pren | ab | push5 | push5
          |---> push3 if end
          |---> push4 if l/end
          |---> push5 if w/end
  9:  <>  | irix | (at) --> ab --> ry | dbi | (pc) --> db --> aob,au | x | +2 --> au | 2b | push3 | popm6
      mmrw3

L
  0:  <>  | twop | au --> db --> aob,au | db | (rxh) --> ab --> dob | x | +2 --> au | reset pren | a5 | stmr4 | stmr6
          |---> stmr4 if l/end
          |---> mmrw2 if end
          |---> stmr5 if w/end
  1:
  2:  >   | frix | alu --> ab* --> alu | db | (at) --> db --> au | bc | edb --> dbin,irc | -1 --> alu | 0 --> au | 2d7 | sccr2 | sccb2
      nbcr3
  3:  >   | frix | au --> db --> au,pc | a1 | (ir) --> ird | x | +2 --> au | e2 | trpv2 | trpv2
  4:  >   | frix | edb --> dbin | db | (pcl) --> ab --> alu | 1n | (sp) --> db --> au | -1 --> alu | -2 --> au | +2 --> psws | 22 | trpv3 | trpv3
      trap3
  5:  >   | frix | alu --> ab* --> alu | db | edb --> dbin,irc | 2n | 0 -- au | 2d3 | sccb2 | sccb2
      morw2
  6:
  7:  >   | frix | (at) --> db --> au | db | au --> pc | x | edb --> dbin,irc | 0 --> au | reset pren | 8 | push6 | push6
      morw2
  8:  <   | twop | au --> db --> aob,at,au | bc | (rxh) --> ab --> dob | x | reset pren | push4 if l/end
          |---> push3 if end
          |----------> push5 if w/end
  9:
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M, N, O, P, Q, R, S